



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. (EC/EC-IOT/EC-VLSI/EE/EI/EX/MX/RA/RA-AI/SE)**

**SEMESTER-I (2024-2028)**

SEMESTER-I (2024-2028)												
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	BTEE106	Fundamentals of Electrical and Electronics Engineering	3	0	2	4	60	20	20	30	20	150
2	BTME101	Engineering Drawing	1	0	4	3	60	20	20	30	20	150
3	BTEC104	Digital Logic and Circuit Design	3	1	2	5	60	20	20	30	20	150
4	BTMA101N	Mathematics I	3	1	0	4	60	20	20	0	0	100
5	HUCS101	Communication Skills	1	0	2	2	60	20	20	0	20	120
6	BTCS101	Computer Programming I	0	0	2	1	0	0	0	30	20	50
TOTAL			11	2	12	19	300	100	100	120	100	720
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical												
*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.												

*[Signature]*  
**Chairperson**  
 Board of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
**Chairperson**  
 Faculty of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
**Controller of Examinations**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
**Registrar**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

*[Signature]*  
**Vice Chancellor**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore  
 10/9/2024